./

GENESIS – Embedded C Learning Report



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| **Ver. Rel. No.** | **Release Date** | **Prepared. By** | **Reviewed By** | **To be Approved** | **Remarks/Revision Details** |
| V1.0 | 02/01/2021 | Shriram M.S |  |  |  |
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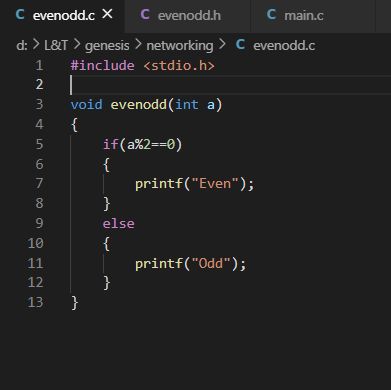
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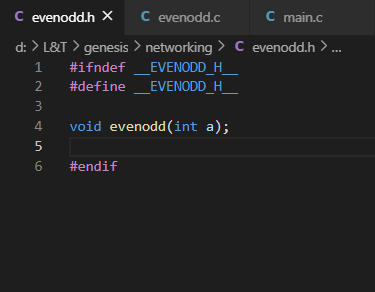
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# ACTIVITY 1: PERFORM BUILD USING GIT BASH

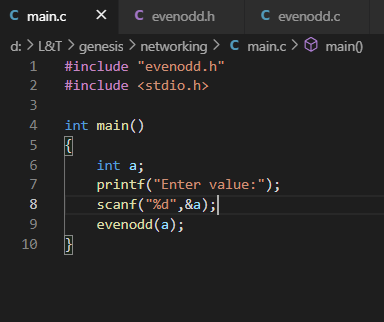
## EVENODD.C FILE

****

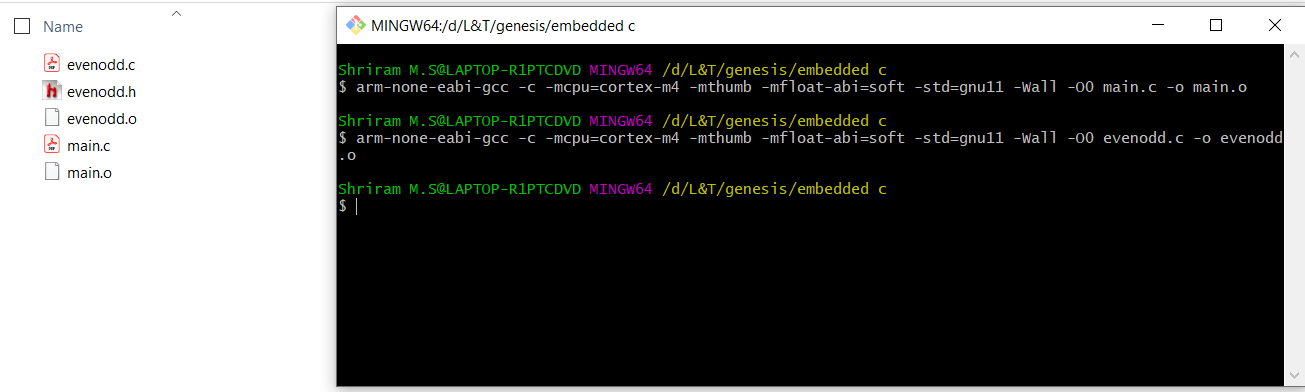
## EVENODD.H FILE



## MAIN FILE



## SCREENSHOT OF BUILD AFTER RUNNING IN GIT BASH



# ACTIVITY 2: LINKER SCRIPT

* The Linker Script is a text file made up of a series of Linker directives which tell the Linker where the available memory is and how it should be used. Thus, they reflect exactly the memory resources and memory map of the target microcontroller.

ENTRY(Reset\_Handler)

MEMORY

{

FLASH(rx):ORIGIN =0x08000000,LENGTH =1024K

SRAM(rwx):ORIGIN =0x20000000,LENGTH =128K

}

SECTIONS

{

.text :

{

\*(.isr\_vector)

\*(.text)

\*(.text.\*)

\*(.init)

\*(.fini)

\*(.rodata)

\*(.rodata.\*)

. = ALIGN(4);

\_etext = .;

}> FLASH

\_la\_data = LOADADDR(.data);

.data :

{

\_sdata = .;

\*(.data)

\*(.data.\*)

. = ALIGN(4);

\_edata = .;

}> SRAM AT> FLASH

.bss :

{

\_sbss = .;

\_\_bss\_start\_\_ = \_sbss;

\*(.bss)

\*(.bss.\*)

\*(COMMON)

. = ALIGN(4);

\_ebss = .;

\_\_bss\_end\_\_ = \_ebss;

. = ALIGN(4);

end = .;

\_\_end\_\_ = .;

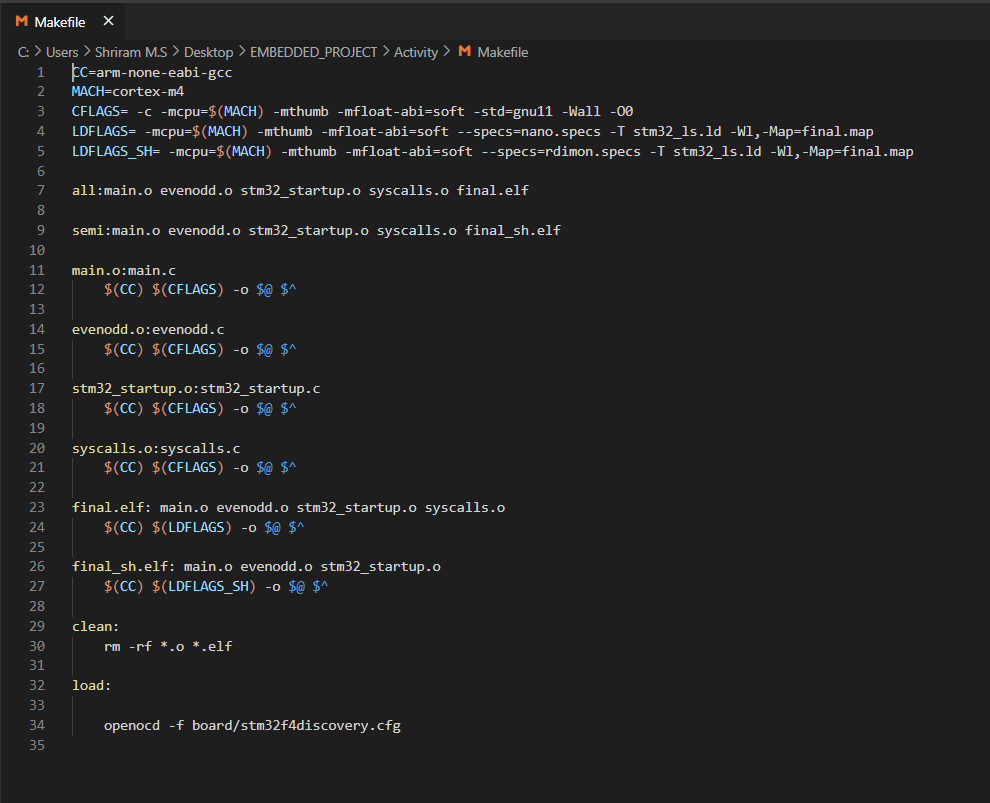
}> SRAM

}

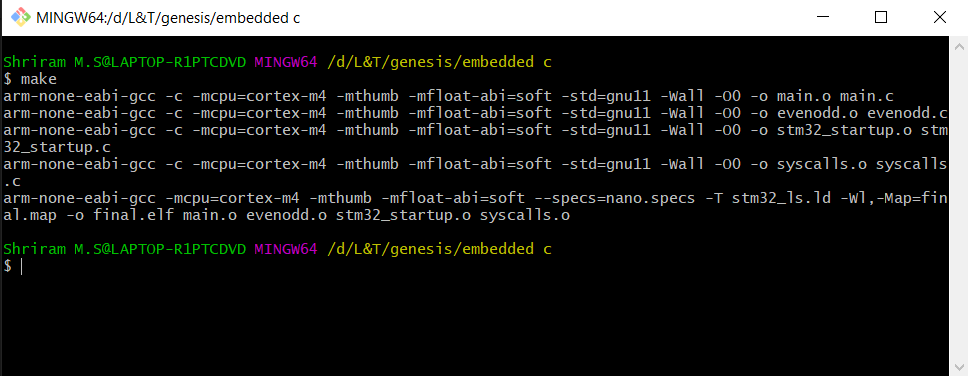
# ACTIVITY 3: STARTUP CODE

# ACTIVITY 4: BUILD USING MAKEFILE

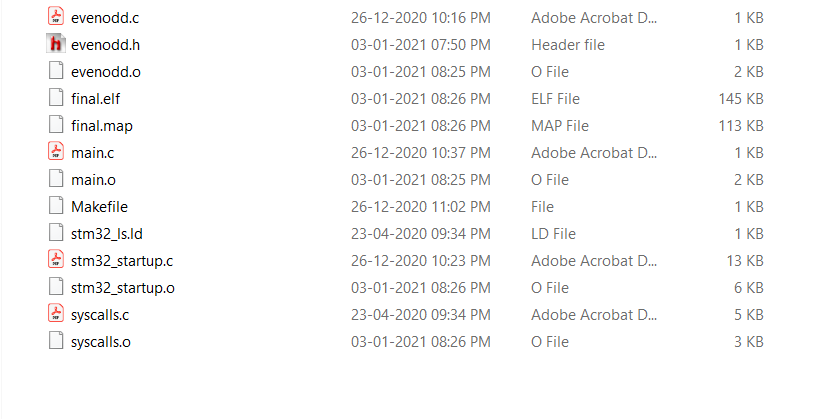
## SCREENSHOT OF MAKEFILE



## SCREENSHOT OF OUTPUT AFTER RUNNING MAKE



## OUTPUT FILES CREATED



## GITHUB LINK

# MINIPROJECT

## SCREENSHOT OF CODE IMPLEMENTATION

## GITHUB LINK